Application No.: 10/723,309 10 Docket No.: 20046/0200609-US0

AMENDMENTS TO THE ABSTRACT

Please substitute the following paragraph(s) for the abstract now appearing in the currently filed specification:

A circuit arrangement comprising a flip-flop, a first power switch transistor, and a plurality of switching transistors. The flip-flop has a plurality of storage transistors with a threshold voltage of a first value. The first power switch transistor has a threshold voltage of a second value, wherein an application of a predetermined electrical potential to its gate terminal brings the circuit arrangement to an operating state such that if at least one supply voltage is switched off, electric charge carriers contained in the circuit arrangement are prevented from flowing away from the circuit arrangement. The plurality of switching transistors, which have a threshold voltage of a third value, are provided between the flip-flop and the first power switch transistor, for coupling a flip-flop input signal into the flip-flop. The magnitude of the first and/or the second value is greater than the magnitude of the third value.